



2631

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

G. Bendak et al.

Group No.: 2631

Serial No.: 09/746,159

Examiner: Unknown

Filed: 22 December 2000

Docket No. AMCC4500

For: System and Method for Translating Overhead Bytes in a Multidimensional Digital Frame Structure

CERTIFICATION UNDER 37 CFR § 1.8

I hereby certify that the documents referred to as enclosed herein are being deposited with the United States Postal Service as first class mail on this date 3/15/01 in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231

3/15/01
Date

[Signature]
Signature

BOX NON-FEE AMENDMENT
Assistant Commissioner for Patents
Washington, D.C. 20231

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Sir:

PRELIMINARY AMENDMENT

In advance of the first examination in this case, Applicants request amendment of the subject application, as follows:

IN THE SPECIFICATION

Please delete the text in the section labeled "RELATED APPLICATIONS" and replace it with the following text:

RELATED APPLICATIONS

This application contains material related to the following commonly assigned U.S. Patent Applications, which were concurrently filed with this application, and are hereby incorporated by reference:

Serial No. 09/746,490, filed 22 December 2000 for "SYSTEM AND METHOD FOR PROGRAMMING THE LOCATION OF FRAME SYNCHRONIZATION WORDS IN A MULTIDIMENSIONAL DIGITAL FRAME STRUCTURE"

[Signature]



Serial No. 09/745,655, filed 22 December 2000 for "SYSTEM AND METHOD FOR PROGRAMMING THE VALUE OF FRAME SYNCHRONIZATION WORDS IN A MULTIDIMENSIONAL DIGITAL FRAME STRUCTURE"

Serial No. 09/747,380, filed 22 December 2000 for "SYSTEM AND METHOD FOR PROGRAMMING THE BIT ERROR RATE OF FRAME SYNCHRONIZATION WORDS IN A MULTIDIMENSIONAL DIGITAL FRAME STRUCTURE"

Serial No. 09/746,152, filed 22 December 2000 for "SYSTEM AND METHOD FOR PROGRAMMING THE QUANTITY OF FRAME SYNCHRONIZATION WORDS IN A MULTIDIMENSIONAL DIGITAL FRAME STRUCTURE"

Serial No. 09/745,793, filed 22 December 2000 for "SYSTEM AND METHOD FOR SELECTIVELY BROADCASTING A MULTIDIMENSIONAL DIGITAL FRAME STRUCTURE"

Serial No. 09/745,774, filed 22 December 2000 for "SYSTEM AND METHOD FOR PROGRAMMING SYNCHRONIZATION CRITERIA IN A MULTIDIMENSIONAL DIGITAL FRAME STRUCTURE"

Serial No. 09/747,072, filed 22 December 2000 for "SYSTEM AND METHOD FOR PROGRAMMING LOSS OF SYNCHRONIZATION IN A MULTIDIMENSIONAL DIGITAL FRAME STRUCTURE"

Serial No. 09/527,343, filed 17 March 2000 for "TRANSPOSABLE FRAME SYNCHRONIZATION STRUCTURE"

Serial No. 09/528,021, filed 17 March 2000 for "PROGRAMMABLE SYNCHRONIZATION STRUCTURE WITH AUXILIARY DATA LINK"

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REMARKS

This Preliminary Amendment is submitted in advance of the first examination of the subject application in order to correct typographical errors and insert the serial numbers and filing dates of the related applications. No new matter has been entered.

Respectfully submitted,

GERALD W. MALISZEWSKI
Reg. No. 38,054

Date: *March 15, 2001*

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**VERSION WITH MARKINGS TO SHOW CHANGES MADE
RELATED APPLICATIONS**

This application contains material related to the following commonly assigned U.S. Patent Applications, which were concurrently filed with this application, and are hereby incorporated by reference:

Serial No. 09/746,490, [] filed 22 December 2000
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09/746,490, 09/745,655, 09/747,380, 09/746,152, 09/745,793

Serial No. 09/745,774, [] filed 22 December 2000

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SYNCHRONIZATION [SYNCHRONIZING] CRITERIA IN_A
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